

The listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

1. (Original) A method of driving a display device comprising:
storing a first image data;
storing a second image data;
comparing the first image data and the second image data;
supplying horizontal and vertical synchronizing signals to a controller from a synchronizing signal generator circuit if the first image data and the second image data are different from each other; and
halting an output of the vertical synchronizing signals from the synchronizing signal generator circuit if the first image data and the second image data coincide with each other.
2. (Original) The method according to claim 1 wherein said display device is a liquid crystal device.
3. (Original) A method of driving a display device comprising:
storing a first image data;
storing a second image data;
comparing the first image data and the second image data;
supplying horizontal and vertical synchronizing signals to a controller from a synchronizing signal generator circuit if the first image data and the second image data are different from each other; and
halting an output of the horizontal and vertical synchronizing signals from the synchronizing signal generator circuit if the first image data and the second image data coincide with each other.

4. (Original) The method according to claim 3 wherein said display device is a liquid crystal device.

5. (Original) A method of driving a display device comprising:
storing a first image data;
storing a second image data;
comparing the first image data and the second image data;
supplying horizontal and vertical synchronizing signals to a peripheral circuit from a synchronizing signal generator circuit if the first image data and the second image data are different from each other; and
halting an output of the vertical synchronizing signals from the synchronizing signal generator circuit if the first image data and the second image data coincide with each other.

6. (Currently Amended) The method according to claim ~~[[4]]~~ 5 wherein said display device is a liquid crystal device.

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7. (Original) A method of driving a display device comprising:
storing a first image data;
storing a second image data;
comparing the first image data and the second image data;
supplying horizontal and vertical synchronizing signals to a peripheral circuit from a synchronizing signal generator circuit if the first image data and the second image data are different from each other; and
halting an output of the horizontal and vertical synchronizing signals from the synchronizing signal generator circuit if the first image data and the second image data coincide with each other.

8. (Original) The method according to claim 7 wherein said display device is a liquid crystal device.

9. (Original) A method of driving a display device comprising:
storing a first image data;
storing a second image data;
comparing the first image data and the second image data;
supplying horizontal and vertical synchronizing signals to a display portion if the first image data and the second image data are different from each other; and
halting the supplying of the vertical synchronizing signals to the display portion if the first image data and the second image data coincide with each other.

10. (Currently Amended) The method according to claim 6 9 wherein said display device is a liquid crystal device.

11. (Currently Amended) The method according to claim 8 9 wherein said display portion is a simple matrix type.

12. (Currently Amended) The method according to claim 8 9 wherein said display portion is an active matrix type.

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13. (Original) A method of driving a display device comprising:
storing a first image data;
storing a second image data;
comparing the first image data and the second image data;
supplying horizontal and vertical synchronizing signals to a display portion if the first and second image data are different from each other; and
halting the supplying of the horizontal and vertical synchronizing signals to the display portion if the first image data and the second image data coincide with each other.

14. (Original) The method according to claim 13 wherein said display device is a liquid crystal device.

15. (Original) The method according to claim 13 wherein said display portion is a simple matrix type.

16. (Original) The method according to claim 13 wherein said display portion is an active matrix type.

17. (Original) A display device comprising:
a display portion;
a first VRAM for storing a first image data;
a second VRAM for storing a second image data;
a comparator circuit for comparing the first and second image data;
a synchronizing signal regulator circuit for selectively outputting horizontal and vertical synchronizing signals depending upon an output from the comparator.

18. (Original) The display device according to claim 17 wherein said display device is a liquid crystal device.

19. (Original) The display device according to claim 17 wherein said synchronizing signal regulator circuit comprises AND circuits.

20. (Original) A display device comprising:
a display portion;
a first VRAM for storing a first image data;
a second VRAM for storing a second image data;
a comparator circuit for comparing the first and second image data;
a synchronizing signal regulator circuit for selectively outputting vertical synchronizing signals depending upon an output from the comparator.

21. (Original) The display device according to claim 20 wherein said display device is a liquid crystal device.

22. (Original) The display device according to claim 20 wherein said synchronizing signal regulator circuit comprises AND circuits.

23. (Currently Amended) A display device comprising:

- a display portion;
- at least one ~~vide~~ video random access memory for storing at least first and second image data;
- a comparator circuit for comparing the first and second image data;
- a synchronizing signal generator circuit for outputting horizontal synchronizing signals and vertical synchronizing signals;
- a synchronizing signal regulator circuit for receiving an output signal from the comparator circuit and the horizontal and vertical synchronizing signals; and
- a controller operationally connected to the synchronizing signal regulator circuit.

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